



Attorney's Docket No.: 0797-010005 / US2941D1D1D1

2822 \$

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kusumoto, et al. Art Unit : Unknown  
Serial No.: 09/941,367 Examiner : Unknown  
Filed : August 28, 2001  
Title : METHOD FOR PRODUCING INSULATED GATE THIN FILM  
SEMICONDUCTOR DEVICE

Commissioner for Patents  
Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

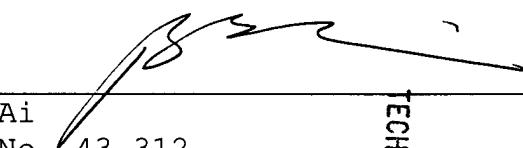
Correspondence relating to this application is enclosed.  
The required fees are computed below. Please apply any charges  
not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	108	-	80	=	28	\$504
Independent	23	-	16	=	7	\$588
First Presentation of Multiple Dependent						
Claims						\$280
TOTAL FEE DUE						\$1372

A check for \$1372 is attached.

Respectfully submitted,

Date: Oct. 10, 2001

  
Bing Ai  
Reg. No. 43,312

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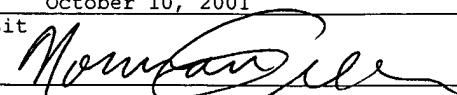
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CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

October 10, 2001

Date of Deposit



Signature

Norman Green

Typed or Printed Name of Person Signing  
Certificate



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#4 / Pre Amndt. B  
 1-12-02  
 P. States

Commissioner for Patents  
 Washington, D.C. 20231

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

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In the claims:

Add claims 21-30 as follows:

B  
 -- 21. (New) A method for manufacturing a semiconductor device, said method comprising the steps of:  
 forming a semiconductor layer over a substrate;  
 irradiating said semiconductor layer with a CW laser beam to crystallize said semiconductor layer; and  
 patterning the crystallized semiconductor layer to form an active layer including a channel region,

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01 FC:103	504.00 OP
02 FC:102	588.00 OP
03 FC:104	280.00 OP